

Form P.T.O. 1449

U.S. Department of Commerce
Patent and Trademark OfficeDocket No.
P01,0062

Serial No.

Applicant
Robert BAUMGARTNER et al.LIST OF PRIOR ART CITED BY APPLICANT
(use several sheets if necessary)

Filing Date

Group Art Unit

U.S. PATENT DOCUMENTS

Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If appropriate
chk	AA	5,572,687	11/05/96	Alnuweiri			

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
chk	AD	WO 92/21088	11/26/92	PCT				
chk	AE	0 287 119	10/19/88	Europe				
	AF							
	AG							
	AH							

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

chk	AI	Packer et al., "Message Passing Supports Multiple Processor Design", Computer Design/June 15, 1984, pp. 117-124.
chk	AJ	IEEE Standard for a High-performance Synchronous 32 Bit Bus: Multibus II.
chk	AK	Bussysteme, Parallele und serielle Bussysteme, lokale Netze, 1987
chk	AL	20 MHZ Multibus II Parallel System Bus Investigation.
	AM	
	AN	
	AO	
	AP	
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Examiner

C. Kuell

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12/2/03

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INFORMATION DISCLOSURE CITATION
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Docket No.
P01,0062

Serial No.
09/807,352

Robert Baumgartner et al.

Filing Date
06/28/2001

Group Art Unit
2151

U.S. PATENT DOCUMENTS

Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If appropriate
	AA						
	AB						
	AC						
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	AH						
	AI						
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FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
chk	AK	WO 91/10 958 A1	07/25/91	PCT				
chk	AL	0 582 535 A1	02/09/94					
	AM							
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	AO							
	AP							
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	AR							
	AS							
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OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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